



Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	09/477,985
Filing Date	01/05/2000
First Named Inventor	Pallab Midya
Group Art Unit	2816
Examiner Name	Minh T. Nguyen
Attorney Docket Number	SC10874TC

[illegible][illegible]

1/15/02

¹ Unique citation designation number. ² See Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.

Form PTO-1449	APPLICANT: Pallab Midya et al.	
LIST OF PATENTS AND PUBLICATIONS FOR INFORMATION DISCLOSURE STATEMENT (Use Several Sheets if Necessary)	ATTY. DOCKET #: SC10874TC	APPL. #: Unknown
	FILING DATE: Concurrently Herewith	GROUP: Unknown

U.S. PTO
09/47/985
01/05/00

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE (#43)	COUNTRY	CLASS	SUBCLASS
AL	WO 98/44626	10/8/98	PCT	H03F	3/217
AM					
AN					
AO					
AP					

OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	Mosley, et al., "Effect of dead time on harmonic distortion in class-D audio power amplifiers," ELECTRONIC LETTERS, pp. 950-952 (1999).
AS	Nielsen, "PEDEC - A Novel Pulse Referenced Control Method for High Quality Digital PWM Switching Power Amplification," IEEE, pp. 200-207 (1998).
AT	Midya et al., "Sensorless Current Mode Control: An Observer-Based Technique for Dc-Dc Converters," Power Electronics Specialists Conference, Annual IEEE, pp. 197-202 (1997).
AU	Kimball, et al., "Continuous-time optimization of gate timing for synchronous rectification," Circuits and Systems, IEEE 39th Midwest symposium, pp. 1015-1018 (1996).
AV	
AW	
AX	
AY	
AZ	

**NOT CONSIDERED
NO COPY ARE PROVIDED**

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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